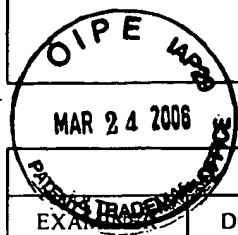


<b>FORM 1449*</b> <b>INFORMATION DISCLOSURE STATEMENT</b>  <b>IN AN APPLICATION</b> (Use several sheets if necessary)	Docket Number: 50019.242US01/P05640	Application Number: 10/651,849
	Applicant: Paul M. Henry	
	Filing Date: August 29, 2003	Group Art Unit: 2816



U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TRC	5,847,670	12/08/98	Gratex et al.	341	159	

FOREIGN PATENT DOCUMENTS							
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
TRC		Grebene, Alan, "Bipolar and MOS Analog Integrated Circuit Design," <i>John Wiley &amp; Sons</i> , 1939, 1984 (9 pgs)
TRC		Razavi, Behzad, "Design of Analog CMOS Integrated Circuits," <i>McGraw Hill</i> , 2001, (5 pgs)
TRC		Li, Tao, et al. A Low Voltage Bandgap Reference Circuit with Current Feedback," <i>University of Michigan Design Contest Report</i> , Fall, 2003 (5 pgs)
TRC		Gupta, Vishal, et al., "Predicting and Designing for the Impact of Process Variations and Mismatch on the Trip Range and Yield of Bandgap References," <i>IEEE Quality of Electronic Design Symposium</i> , March 21-23, 2005 (7 pgs)
TRC		"Offset Null," <i>Answers Corporation</i> , 2006 (2 pgs)

<b>23552</b> PATENT TRADEMARK OFFICE
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EXAMINER <u>T. Cunningham</u>	DATE CONSIDERED <u>4/5/06</u>
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.	